

U.S. Department of Commerce, Patent and Trademark Office					Application No.:		UNKNOWN	
					Filing Date:		October 27, 2003	
INFORMATION DISCLOSURE STATEMENT BY					Inventor:		Donald S. Stern	
(Use several sheets if necessary)					Group Art Unit:		2824	
					Examiner Name:		Smith, Bradley	
					Attorney Docket		GRA003-2D US	
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date	
	1.	4,153,934	5/8/79	Sato	364	200	1/27/77	
	2.	4,352,238	10/5/82	Shimbo	29	579	4/14/80	
	3.	4,807,015	2/21/89	Kobayashi	357	67	5/13/87	
	4.	5,418,376	5/23/95	Muraoka	257	136	2/28/94	
	5.	5,442,211	8/15/95	Kita	257	301	11/23/93	
	6.	5,600,166	2/4/97	Clementi	257	324	7/1/95	
	7.	5,607,773	3/4/97	Ahlburn	428	427	12/20/94	
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	8.	Book entitled "Nonvolatile Semiconductor Memory Technology", Edited by William D. Brown and Joe. E. Brewer and published by IEEE Press (1998), ISBN 0-7803-1173-6, pages 310-313, and pages 363-398.						
	9.	Article entitled "Applied Materials Introduces New Storage Capacitor Solution for Gigabit DRAMs" dated July 8, 1998 from Applied Material Corporation web site.						
	10.	Article entitled "Feature-Rich Flash Memories Deliver High Density" by Dave Bursky, Electronic Design, dated August 9, 1999, Volume 47, Number 16.						
	11.	Book entitled "Flash Memories", Chapter 4 entitled "Physical Aspects of Cell operation and reliability" by Paolo Cappelletti et al. Kluwer Academic Publishers, 1999, pages 157-160.						
	12.	Book entitled "Fundamentals of Electronics", by E. Norman Lurch, John Wiley and Sons, Inc, Second Edition, 1971, Chapter 9 at page 236						
Examiner			Date Considered					
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.								

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	13.	5,756,385	5/26/98	Yuan	438	258	11/22/95	
	14.	5,834,992	11/10/98	Kato et al.	333	185	12/27/96	
	15.	5,854,503	12/29/98	Hsueh	257	347	11/19/96	
	16.	5,856,221	1/5/99	Clementi	438	258	7/20/96	
	17.	6,004,847	12/21/99	Clementi	438	258	7/20/96	
	18.	6,008,514	12/28/99	Wu	257	308	4/27/99	
	19.	6,064,085	5/16/00	Wu	257	296	6/3/98	
	20.	6,064,589	5/16/00	Walker	365	149	5/5/98	
	21.	6,072,210	6/6/00	Choi	257	303	12/24/98	
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							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
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	22.	Book entitled "Flash Memories" by Paolo Cappelletti et al. Kluwer Academic Publishers, 1999, Chapter 6, Pages 363-375.						
	23.	Book entitled "Semiconductor Memories" by Askok K. Sharma, IEEE Presses, 1997, Pages 50-57						
	24.	Book entitled "Semiconductor Memories" by Betty Prince, Wiley Publishers, 1991, Pages 262-264.						
	25.	Book entitled "CMOS MEMORY CIRCUITS" by Tegze P. Haraszti, Kluwer Academic Publishers, 2000, Pages 105-109.						
Examiner			Date Considered					
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	27.	6,097,049	8/1/00	Goebel et al.	257	296	3/18/99	
	28.	6,121,651	9/19/00	Furukawa et al.	257	296	7/30/98	
	29.	6,137,131	10/24/00	Wu	257	309	12/29/97	
	30.	6,143,617	11/7/00	Shue et al.	438	396	2/23/98	
	31.	6,144,054	11/7/00	Agahi et al.	257	296	12/4/98	
	32.	6,147,376	11/14/00	Hofman et al.	257	306	1/12/99	
	33.	6,150,208	11/21/00	Deboer et al.	438	240	5/11/98	
	34.	6,157,565	12/5/00	Wu et al.	365	175	9/15/99	
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	36.	2002/0000556	1/3/02	Sakamoto et al.	257	66	6/15/01	
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	38.	6,054,750	4-2000	Imam et al.	257	531			
	39.	6,249,039	06-2001	Harvey et al.	257	531			
	40.	6,380,578	04-2002	Kunikiyo	257	301			
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